IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Manrique J. Brenes; Yen T. Nguyen

Assignee: Cisco Technology, Inc.

Title: METHOD AND SYSTEM FOR REAL-TIME BIT ERROR
                   RATIO DETERMINATION

Serial No.:   10/773,705   Filing Date: February 6, 2004

Examiner: Stephen M. Baker   Group Art Unit: 2112

Docket No.:   CIS0209US   Confirmation No.   8310

Austin, Texas
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RESPONSE TO FINAL OFFICE ACTION

Dear Examiner Baker:

This paper is responsive to the Office Action dated April 15, 2008, having a shortened statutory period expiring on July 15, 2008. This Response is being filed within two months of the mailing date of the Office Action, since June 15, 2008 was a Sunday. Further examination and reconsideration are respectfully requested in view of the amendments and remarks set forth below.

No Amendments to the Specification are presented in this paper.

Amendments to the Claims are presented in the listing of claims beginning on page 2 of this paper.

No Amendments to the Drawings are presented in this paper.

Remarks begin on page 9 of this paper.

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